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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/520,867	01/11/2005	Arjan Van Den Berg	NL 020647	6498
24737	7590	08/24/2006		EXAMINER
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			VU, JIMMY T	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
BRIARCLIFF MANOR, NY 10510			2821	

DATE MAILED: 08/24/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/520,867	VAN DEN BERG, ARJAN	
Examiner	Art Unit		
Jimmy T. Vu	2821		

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### **Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 11 January 2005.

2a)  This action is FINAL.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

4)  Claim(s) 1-5 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1,2,4 and 5 is/are rejected.

7)  Claim(s) 3 is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 1/26/06

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_ .

5)  Notice of Informal Patent Application (PTO-152)

6)  Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 2, 4 and 5 are rejected under 35 U.S.C. 102(e) as being anticipated by Kohn (U.S. Patent number 6,747,421 B1).

Regarding claim 1, Kohn discloses ballast circuit for operating a gas discharge lamp, comprising:

a half-bridge DC-AC converter (Fig. 1) having a voltage controlled oscillator for alternately switching the switches of said half-bridge, said oscillator having an input with a control voltage which determines the operating frequency of said half-bridge;

a resonance circuit (10, C2, R22) connected to said half-bridge for feeding the lamp; and

a feedback circuit (L, C1) connected at a first end to said resonance circuit for adjusting the operating frequency of said half-bridge,

characterized in that

the other end of said feedback circuit is connected to the input of said voltage controlled oscillator and desired such that during at least a substantial part of the start-up period of the lamp an equilibrium exists wherein the half-bridge frequency is at least nearly equal to the resonance frequency and the half-bridge voltage is forced to operate at least nearly in phase with the half-bridge current (Figs. 1 and 2, col. 3, lines 1-67).

Regarding claim 2, Kohn discloses ballast circuit characterized in that the first end of the feedback circuit is connected to the serial connection between the two switches of the half-bridge (Fig. 1).

Regarding claim 4, Kohn discloses ballast circuit characterized in that the ballast circuit is inherently integrated in an IC.

Regarding claim 5, Kohn discloses the lamp driver comprising the ballast circuit (Figs. 1-11).

#### ***Allowable Subject Matter***

3. Claims 71 and 72 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

None of the prior art teaches the ballast circuit characterized in that the oscillator input is further connected to the current supply and the capacitor, wherein the equilibrium is determined by the current supply loading the capacitor, and the feedback circuit at least partially unloading the capacitor each half-bridge switching cycle.

***Information Disclosure Statement***

4. The references listed on the information disclosure statement submitted on 01/26/2006 have been considered.

***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy T Vu whose telephone number is (571) 272-1832. The examiner can normally be reached on M - F: 9 - 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone numbers for the organization where this application or proceeding is assigned are (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-2800.

Jimmy Vu

August 17, 2006



TUYET VO  
PRIMARY EXAMINER